



Real-Time Emulator Zilog® Z80

MODEL 64250A
OPTION 252

TECHNICAL DATA 1 OCT 81

Description

Model 64250A Option 252 Emulator provides functionally transparent, real-time emulation of the Zilog Z80 microprocessors. Each emulator consists of an emulation control board and emulator pod assembly. High-speed emulation memory, which is ordered separately, allows emulation of the processor at specified speeds without insertion of wait states. The 64250A Emulation System is compatible with target systems using processors that meet specifications of Zilog Z80.

Features

Software control of host/target system memory mapping.

- Map in 1k-byte blocks over 64k-byte address space

- Designate blocks as RAM, ROM, or illegal memory

Symbolic debugging capability with both assembly language and Pascal programs.

Real-time emulation without wait states at specified speeds.

Simulated I/O for emulator access to HP 64000 system resources: disc files, printer, development station keyboard and display, and RS-232-C port.

Fast downloading of programs into emulation and/or target system memory.

Powerful logic analysis for complex program tracing.

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Characteristics

ELECTRICAL

Maximum Clock Speeds

With no wait states inserted: 4 MHz with all memory assigned to target system; 3.6 MHz with any memory assigned to emulation memory.

With wait states inserted: 4 MHz. (Wait states inserted only when accessing emulation memory.)

Emulation Pod to Target System Interface: low power Schottky TTL levels with capacitance of approx 20 pF, except clock inputs which meet Zilog Z80A specifications plus capacitance of approx 20 pF.

PHYSICAL

Cable Length: development station to emulation pod, approx 1.5 m (5 ft); emulation pod to target system interface, approx 0.3 m (1 ft).

ENVIRONMENTAL

Temperature: operating, 0° to 40° C (+32° to +104° F); nonoperating, -40° to +75° C (-40° to +167° F); operating survival, -20° to +50° C (-4° to +122° F).

Altitude: operating, 4600 m (15 000 ft); nonoperating, 15 300 m (50 000 ft).

Relative Humidity: 5% to 80%.



Ordering Information

FACTORY INSTALLED

Model 64001S Logic Development System

Option 252:	Z80 Emulation System and Assembler	\$3000
Option 151:	64k Byte Emulation Memory System	\$6400
Option 152:	32k Byte Emulation Memory System	\$3400
Option 153:	16k Byte Emulation Memory System	\$2300
Option 154:	8k Byte Emulation Memory System	\$1600
Option 300:	Logic Analyzer	\$1800
Option 812:	Z80 Pascal/64000 Compiler	\$2000
Option 842:	Z80 Assembler/Linker/Emulation Software	\$500

Model 64005S Emulation Terminal System

Option 252:	Z80 Emulation System with Terminal/Emulation Software	\$3000
Option 151:	64k Byte Emulation Memory System	\$6400
Option 152:	32k Byte Emulation Memory System	\$3400
Option 153:	16k Byte Emulation Memory System	\$2300
Option 154:	8k Byte Emulation Memory System	\$1600
Option 300:	Logic Analyzer	\$1800

USER INSTALLED

Model 64250A Z80 Family Emulation System

Option 252:	Z80 Emulation System and Assembler	\$3000
Option 253:	Z80 Emulation System without Software	\$2500

Model 64251A Z80 Emulator Control Board

		\$1100
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Model 64252A Z80 Emulator Pod

		\$1400
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Model 64150A Emulation Memory System

Option 151:	64k Byte Emulation Memory System	\$6475
Option 152:	32k Byte Emulation Memory System	\$3450
Option 153:	16k Byte Emulation Memory System	\$2350
Option 154:	8k Byte Emulation Memory System	\$1650

Model 64300A Logic Analyzer

		\$1800
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Model 64812A Z80 Pascal/64000 Compiler

		\$2000
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Model 64842B Z80 Assembler/Linker/Emulation Software

		\$550
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Your HP Field Engineer can help you determine the best configuration to meet your needs.

Prices apply only to domestic U.S. customers.